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A-75030

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of
CHIOU-FENG CHEN ET AL.

Serial No. 10/803,183

Filed: March 17, 2004

For: SELF-ALIGNED SPLIT-GATE
NAND FLASH MEMORY AND
FABRICATION PROCESS

Examiner: Hien N. Nguyen

Group Art Unit: 2824

Confirmation No. 5108

September 9, 2005

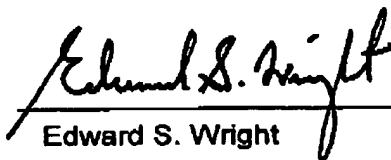
RESPONSE TO OFFICE ACTIONCommissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action mailed August 24, 2005, applicant is submitting a terminal disclaimer to overcome the double patenting rejection. Since that is the only ground upon which the claims were rejected, applicant trusts that the claims will now be allowed and the application will be passed to issue.

The Commissioner is authorized to charge any fees required in connection with this matter, including extension fees, to Deposit Account No. 50-2975, Order No. A-75030.

Respectfully submitted,



Edward S. Wright

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CERTIFICATE OF FACSIMILE TRANSMISSION

I CERTIFY THAT THIS RESPONSE IS BEING FORWARDED TO THE PATENT OFFICE FOR FILING VIA FACSIMILE TRANSMISSION TO (571) 273-8300 ON September 9, 2005.


EDWARD S. WRIGHT